



L Number	Hits	Search Text	DB	-Time stamp
40	38	master\$1 with slave\$1 with control\$5 with (fpga\$1 or	USPAT;	2004/05/27
		pld\$1 or pga\$1 or epld\$1)	US-PGPUB;	13:19
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
41	34	(master\$1 with slave\$1 with control\$5 with (fpga\$1	USPAT;	2004/05/27
		or pld\$1 or pga\$1 or epld\$1)) and (@ad<20010501 or	US-PGPUB;	13:21
		@rlad<20010501 or @prad<20010501 or	EPO; JPO;	
		@pt1d<20010501 or @pd<20010501)	DERWENT;	
			IBM_TDB	
42	7027	control\$5 with (fpga\$1 or pld\$1 or pga\$1 or epld\$1)	USPAT;	2004/05/27
			US-PGPUB;	13:26
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
43	1790	(control\$5 with (fpga\$1 or pld\$1 or pga\$1 or epld\$1))	USPAT;	2004/05/27
	1, 50	with (implement\$5 or compris\$5)	US-PGPUB;	13:20
		with (implement 45 or comprises)	EPO; JPO;	13.20
			DERWENT:	
ŀ			IBM_TDB	
44	16	((control\$5 with (fpga\$1 or pld\$1 or pga\$1 or epld\$1))	USPAT;	2004/05/27
77	10	, , , , , , , , , , , , , , , , , , , ,		
		with (implement\$5 or compris\$5)) same master\$1	US-PGPUB;	13:20
		same slave\$1	EPO; JPO;	
			DERWENT;	
45	12	/// +	IBM_TDB	0004/05/07
45	13	(((control\$5 with (fpga\$1 or pld\$1 or pga\$1 or	USPAT;	2004/05/27
		epld\$1)) with (implement\$5 or compris\$5)) same	US-PGPUB;	13:27
		master\$1 same slave\$1) and (@ad<20010501 or	EPO; JPO;	
		@rlad<20010501 or @prad<20010501 or	DERWENT;	
	_	@pt1d<20010501 or @pd<20010501)	IBW_TDB	
46	8	((((control\$5 with (fpga\$1 or pld\$1 or pga\$1 or	USPAT;	2004/05/27
-		epld\$1)) with (implement\$5 or compris\$5)) same	US-PGPUB;	13:21
		master\$1 same slave\$1) and (@ad<20010501 or	EPO; JPO;	
		@rlad<20010501 or @prad<20010501 or	DERWENT;	
		@pt1d<20010501 or @pd<20010501)) not (master\$1	IBM_TDB	
		with slave\$1 with control\$5 with (fpga\$1 or pld\$1 or		
		pga\$1 or epld\$1))		
47	713	((control\$5 with (fpga\$1 or pld\$1 or pga\$1 or epld\$1))	USPAT;	2004/05/27
		with (implement\$5 or compris\$5)) with (cpu\$1 or	US-PGPUB;	13:30
+		microprocess\$3 or mpu\$1 or dsp\$1 or process?r\$1 or	EPO; JPO;	
		(processing adj2 (unit\$1 or device\$1 or agent\$1 or	DERWENT;	
		node\$1 or module\$1)))	IBM_TDB	
48	15	(((control\$5 with (fpga\$1 or pld\$1 or pga\$1 or	USPAT;	2004/05/27
		epld\$1)) with (implement\$5 or compris\$5)) with	US-PGPUB;	13:27
		(cpu\$1 or microprocess\$3 or mpu\$1 or dsp\$1 or	EPO; JPO;	
		process?r\$1 or (processing adj2 (unit\$1 or device\$1	DERWENT;	
		or agent\$1 or node\$1 or module\$1)))) with instead	IBM_TDB	





49	13	((((control\$5 with (fpga\$1 or pld\$1 or pga\$1 or	USPAT;	2004/05/27
		epld\$1)) with (implement\$5 or compris\$5)) with	US-PGPUB;	13:31
		(cpu\$1 or microprocess\$3 or mpu\$1 or dsp\$1 or	EPO; JPO;	
		process?r\$1 or (processing adj2 (unit\$1 or device\$1	DERWENT;	
		or agent\$1 or node\$1 or module\$1)))) with instead)	IBM_TDB	
		and (@ad<20010501 or @rlad<20010501 or		
		@prad<20010501 or @pt1d<20010501 or		
		@pd<20010501)		
50	2370	(control\$5 with (fpga\$1 or pld\$1 or pga\$1 or epld\$1))	USPAT;	2004/05/27
		with (cpu\$1 or microprocess\$3 or mpu\$1 or dsp\$1 or	US-PGPUB;	13:30
		process?r\$1 or (processing adj2 (unit\$1 or device\$1	EPO; JPO;	10.00
		or agent\$1 or node\$1 or module\$1)))	DERWENT;	
	·	or agent or node of a module of 1)))	IBM_TDB	
51	194	((control\$5 with (fpga\$1 or pld\$1 or pga\$1 or epld\$1))	USPAT;	2004/05/27
31	134			1
		with (cpu\$1 or microprocess\$3 or mpu\$1 or dsp\$1 or	US-PGPUB;	13:42
		process?r\$1 or (processing adj2 (unit\$1 or device\$1	EPO; JPO;	
		or agent\$1 or node\$1 or module\$1)))) with (instead or	DERWENT;	
	40-	replac\$7 or plac\$7)	IBW_TDB	
52	127	((control\$5 with (fpga\$1 or pld\$1 or pga\$1 or epld\$1))	USPAT;	2004/05/27
		with (cpu\$1 or microprocess\$3 or mpu\$1 or dsp\$1 or	US-PGPUB;	13:31
		process?r\$1 or (processing adj2 (unit\$1 or device\$1	EPO; JPO;	
		or agent\$1 or node\$1 or module\$1)))) with instead	DERWENT;	•
			IBW_TDB	
53	115	(((control\$5 with (fpga\$1 or pld\$1 or pga\$1 or	USPAT;	2004/05/27
		epld\$1)) with (cpu\$1 or microprocess\$3 or mpu\$1 or	US-PGPUB;	13:42
		dsp\$1 or process?r\$1 or (processing adj2 (unit\$1 or	EPO; JPO;	
		device\$1 or agent\$1 or node\$1 or module\$1)))) with	DERWENT;	
		instead) and (@ad<20010501 or @rlad<20010501 or	IBM_TDB	
		@prad<20010501 or @pt1d<20010501 or		
		@pd<20010501)		
54	102	((((control\$5 with (fpga\$1 or pld\$1 or pga\$1 or	USPAT;	2004/05/27
		epld\$1)) with (cpu\$1 or microprocess\$3 or mpu\$1 or	US-PGPUB;	13:42
		dsp\$1 or process?r\$1 or (processing adj2 (unit\$1 or	EPO; JPO;	
		device\$1 or agent\$1 or node\$1 or module\$1)))) with	DERWENT;	
		instead) and (@ad<20010501 or @rlad<20010501 or	IBM_TDB	
		@prad<20010501 or @pt1d<20010501 or	1011_100	
		@pd<20010501)) not (((((control\$5 with (fpga\$1 or		
		pld\$1 or pga\$1 or epld\$1)) with (implement\$5 or		
	:	compris\$5)) with (cpu\$1 or microprocess\$3 or mpu\$1		
		or dsp\$1 or process?r\$1 or (processing adj2 (unit\$1		İ
		or device\$1 or agent\$1 or node\$1 or module\$1)))) with		
		instead) and (@ad<20010501 or @rlad<20010501 or		
		@prad<20010501 or @pt1d<20010501 or		
		@pd<20010501))		
55	168	((control\$5 with (fpga\$1 or pld\$1 or pga\$1 or epld\$1))	USPAT;	2004/05/27
		with (cpu\$1 or microprocess\$3 or mpu\$1 or dsp\$1 or	US-PGPUB;	13:42
		process?r\$1 or (processing adj2 (unit\$1 or device\$1	EPO; JPO;	
		or agent\$1 or node\$1 or module\$1)))) with (instead or	DERWENT;	
		replac\$7 or substitu\$7)	IBM_TDB	





56	155	(((control\$5 with (fpga\$1 or pld\$1 or pga\$1 or	USPAT;	2004/05/27
		epld\$1)) with (cpu\$1 or microprocess\$3 or mpu\$1 or	US-PGPUB;	13:42
		dsp\$1 or process?r\$1 or (processing adj2 (unit\$1 or	EPO; JPO;	
		device\$1 or agent\$1 or node\$1 or module\$1)))) with	DERWENT;	
		(instead or replac\$7 or substitu\$7)) and	IBM_TDB	
		(@ad<20010501 or @rlad<20010501 or		
		@prad<20010501 or @pt1d<20010501 or		
		@pd<20010501)		
57	40	((((control\$5 with (fpga\$1 or pld\$1 or pga\$1 or	USPAT;	2004/05/27
		epld\$1)) with (cpu\$1 or microprocess\$3 or mpu\$1 or	US-PGPUB;	13:43
		dsp\$1 or process?r\$1 or (processing adj2 (unit\$1 or	EPO; JPO;	
		device\$1 or agent\$1 or node\$1 or module\$1)))) with	DERWENT;	
		(instead or replac\$7 or substitu\$7)) and	IBM_TDB	
		(@ad<20010501 or @rlad<20010501 or		
		@prad<20010501 or @pt1d<20010501 or		
		@pd<20010501)) not ((((((control\$5 with (fpga\$1 or		
		pld\$1 or pga\$1 or epld\$1)) with (implement\$5 or		
	•	compris\$5)) with (cpu\$1 or microprocess\$3 or mpu\$1		
		or dsp\$1 or process?r\$1 or (processing adj2 (unit\$1		
· ·		or device\$1 or agent\$1 or node\$1 or module\$1)))) with		
		instead) and (@ad<20010501 or @rlad<20010501 or		
		@prad<20010501 or @pt1d<20010501 or		
		@pd<20010501)) or ((((control\$5 with (fpga\$1 or		
		pld\$1 or pga\$1 or epld\$1)) with (cpu\$1 or		,
		microprocess\$3 or mpu\$1 or dsp\$1 or process?r\$1 or		
		(processing adj2 (unit\$1 or device\$1 or agent\$1 or		
		node\$1 or module\$1)))) with instead) and		
		(@ad<20010501 or @rlad<20010501 or		
		@prad<20010501 or @pt1d<20010501 or		
		@pd<20010501)))		